



VRef	HS1	HS2	Steps	Interpolation	ChopperMode
>= 2.50V	100%	1.77A RMS	8	Yes to 256	stealthChop2
1.25V	50%	8.86A RMS	8	Yes to 256	stealthChop2
0.50V	20%	8.35A RMS	16	Yes to 256	stealthChop2
0.25V	10%	8.35A RMS	16	Yes to 256	stealthChop2

EN (with pull-up) driver enabled
VCC other disabled
PDN/UART (with pull-down) automatic standstill current reduction
VCC automatic standstill power-down disable
optional UART Interface
CLK (with pull-down) Internal clock
optional supply external clock

To access all other nodes (eg spreadCycle) you have to use the UART Interface.

